



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
United States Patent and Trademark Office  
Address: COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450  
www.uspto.gov

## \*BIBDATASHEET\*

CONFIRMATION NO. 6708

Bib Data Sheet

SERIAL NUMBER 10/717,375	FILING DATE 11/19/2003  RULE	CLASS 257	GROUP ART UNIT 2818	ATTORNEY DOCKET NO. 62601-CON2
-----------------------------	---------------------------------------	--------------	------------------------	-----------------------------------

## APPLICANTS

Robert J. Mears, Wellesley, MA;

Jean Augustin Chan Sow Fook Yiptong, Waltham, MA;  
Marek Hytha, Brookline, MA; Scott A. Kreps, Southborough, MA;  
Ilija Dukovski, Newton, MA;

TTN

## \*\* CONTINUING DATA \*\*\*\*\*

This application is a CON of 10/647,060 08/22/2003  
which is a CIP of 10/603,696 06/26/2003  
and is a CIP of 10/603,621 06/26/2003

TTN

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

No

TTN

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* SMALL ENTITY \*\*

\*\* 03/02/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY MA	SHEETS DRAWING 9	TOTAL CLAIMS 26	INDEPENDENT CLAIMS 2
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature _____ Initials TTN				

## ADDRESS

Christopher F. Regan  
Allen, Dyer, Doppelt, Milbrath, Gilchrist, P.A.  
P.O. Box 3791  
Orlando, FL  
32802-3791

## TITLE

Semiconductor device including band-engineered superlattice

FILING FEE

RECEIVED  
439

FEES: Authority has been given in Paper  
No. \_\_\_\_\_ to charge/credit DEPOSIT ACCOUNT  
No. \_\_\_\_\_ for following:

<input type="checkbox"/> All Fees
<input type="checkbox"/> 1.16 Fees ( Filing )
<input type="checkbox"/> 1.17 Fees ( Processing Ext. of time )
<input type="checkbox"/> 1.18 Fees ( Issue )
<input type="checkbox"/> Other _____